



Partner: QSC Q-SYS
Model: Q-SYS Cores
Device Type: DSP

GENERAL INFORMATION

SIMPLWINDOWS NAME:	Q-sys [Core] V4.0
CATEGORY:	Q-SYS V4
VERSION:	V4.0
SUMMARY:	This Module is used for connecting to a Q-SYS core and facilitate control module traffic.
GENERAL NOTES:	<p>This Module provides Connection to any Q-SYS Core. This module works with a multitude of "Control Modules" for controlling Levels, Buttons, Text Fields, etc., of which are setup using Q-SYS Designer Software from QSC.</p> <p>This module has a built in reconnect feature. If connection is lost, it will reattempt every 10 seconds, until it reconnects. It will also attempt to connect if a command is sent between automatic attempts.</p> <p>For any issues please check error logs of processor. The logs may contain detailed info to what the error is.</p>
CRESTRON HARDWARE REQUIRED:	Ethernet Card – 3-Series Only
SETUP OF CRESTRON HARDWARE:	3-Series Only
VENDOR FIRMWARE:	Unknown
VENDOR SETUP:	Setup of Q-SYS Design file, "Named Controls" of desired components for control.



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CONTROL:

Initialize	D	Pulse to Initialize connection to the Q-SYS Core.
[Poll_Rate]	A	Optional - (Tenths of a second) – Changes frequency of automatic polling groups of desired objects. Internal default is .5 seconds, or 5d. 5d is the recommended minimum to maintain processor performance, any faster processor performance may slow, depending on amount of data being processed.
[Core_Custom_String]	S	Optional – Properly formatted JSON string to be sent to Q-SYS core (Refer to QRC documentation within Q-SYS designer software).
[Core_IP Address]	S	Optional – Overrides IP Address property. This will kill existing connections, and attempt to reestablish at new IP address. This can be useful for a program that needs to talk to multiple systems of the same type, but multiple simultaneous connections isn't desired. Also used with the "Q-SYS Backup Core Handler" module. Via Q-SYS software if main core fails, this signal will be used to register new IP address of backup core.

FEEDBACK:

Initialized	D	Latches high to indicate current initialization status. Also best used to initialize named control modules. (See "Control Modules" help file).
[Connected]	D	Optional - Latches high to indicate Q-SYS Core has been connected.
[Redundant_Core]	D	Optional - Indicates is current connected unit is a redundant core.
[Emulator]	D	Optional – Indicates if current connection is an emulator
[Error]	D	Optional – Indicates if an error has been reported. (check error logs for more details)
[Core_Platform]	S	Optional – Indicates type of core connected to
[Core_State]	S	Optional – Indicates the status of the Core, this data is from the Core itself.
[Core_Design_Name]	S	Optional – Indicates the current running Q-SYS designer file name



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PARAMETERS:

Core ID	Dec	Used to Address Core Module to Control Modules.
Login	S	String used by Q-SYS for login to the Core (if required by Q-SYS design). See Q-SYS administrator
Login Pin	Dec	Number used by Q-SYS for login to the Core (if required by Q-SYS design).
Core IP Address	S	Default IP Address for connection

TESTING:

OPS USED FOR TESTING:	v4.001.1012
SIMPL WINDOWS USED FOR TESTING:	4.07.03
DEVICE DB USED FOR TESTING:	85.05.003.00
CRES DB USED FOR TESTING:	63.05.007.0
SYMBOL LIBRARY USED FOR TESTING:	1035
SAMPLE PROGRAM:	Q-SYS V4.0 Demo Program
REVISION HISTORY:	V4.0 – Completely Revamped Module Set.